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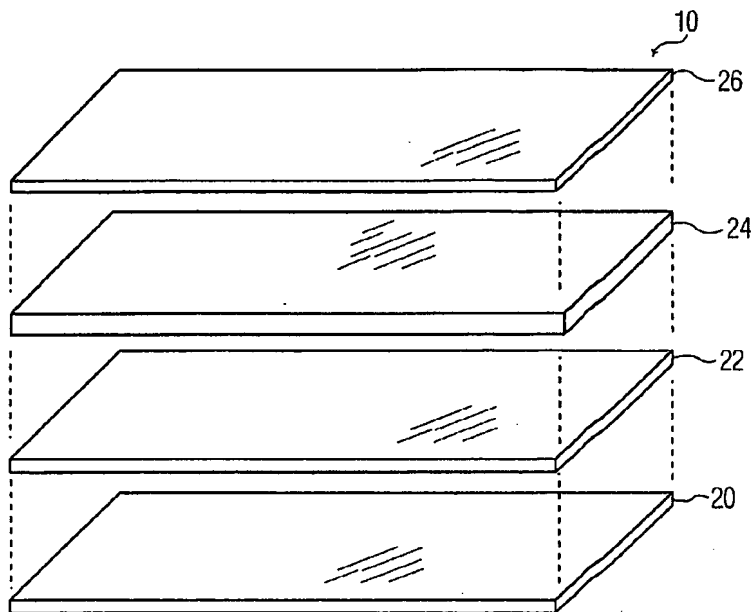
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(54) Title: ELECTRONIC PAINT STRUCTURE WITH THERMAL ADDRESSING LAYER



(57) Abstract: An electronic paint for an electrophoretic display includes a lower conductive layer, a thermal addressing layer disposed on the lower conductive layer, a layer of electrophoretic ink disposed on the thermal addressing layer, and an upper conductive layer disposed on the electrophoretic ink. Activation of the electrophoretic ink is based on thermal absorption of thermal radiation in a portion of the thermal addressing layer and a bias voltage applied between the upper conductive layer and the lower conductive layer.



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Declarations under Rule 4.17:

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA,

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- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations

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